



03-18-05

IFW #

Attorney Docket No.: 254-094-CIP4-C1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Memory Expansion and Chip Scale Stacking System and Method

Application No.: 10/814,530

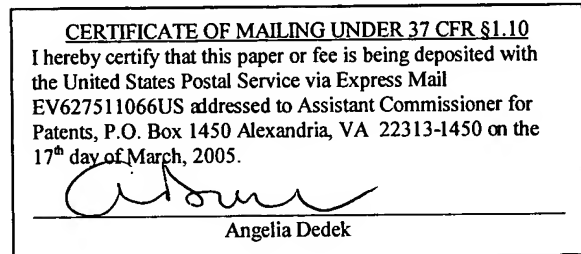
Filing Date: 03/31/04

Inventor: Rapport, et al

Assignee: Staktek Group L.P.

Examiner: Thanh Y. Tran

Group: 2822



**RESPONSE TO OFFICE ACTION DATED DECEMBER 17, 2004**

Assistant Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Applicant responds as follows to the December 17, 2004 office action in the above-identified patent application. Applicant presents amendments and arguments to overcome the Examiner's various objections and rejections.

Please enter the Amendments listed below.

Amendments to the **Specification** start on **page 2** of this paper.

Amendments to the **Drawings** are instructed on **page 5** of this paper and amended drawings labeled "replacement sheet" are included with this paper.

Amendments to the **Claims** begin on **page 6** of this paper.

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